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Prepared by Jannotti
Date 6/23/04
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Week Date 6/7/04
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JACKET			
a. Serial No.	f. Foreign Priority	k. Print Claim(s)	p. PTO-1449
b. Applicant(s)	g. Disclaimer	l. Print Fig.	q. PTOL-85b
c. Continuing Data	h. Microfiche Appendix	m. Searched Column	r. Abstract
d. PCT	i. Title	n. PTO-270/328	s. Sheets/Figs
e. Domestic Priority	j. Claims Allowed	o. PTO-892	t. Other

MESSAGE

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- c. Holes through Data
- d. Other Missing Text
- e. Illegible Text
- f. Duplicate Text
- g. Brief Description
- h. Sequence Listing
- i. Appendix
- j. Amendments
- k. Other

RESPONSE

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- c. Duplicate Numbers
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1 of 7

Complete If Known

Application / C. nf. No.	10/075,178 / 7288
Filing Dat	February 14, 2002
First Named Invent r	Andy H. Gan
Art Unit	2825
Examiner Name	Binh C. Tat
Attorney Docket Number	X-1068 US

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. 1	Document Number Number - Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US- 4,758,985	07-19-88	Carter	
		US- 5,142,625	08-25-92	Nakai	
		US- 4,855,669	08-08-89	Mahoney	
		US- 34,363	08-31-93	Freeman	
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		US- 6,467,009	10-15-02	Winegarden et al.	
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		US- 5,742,179	04-21-98	Sasaki	
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		US- 5,574,930	11-12-96	Halverson Jr., et al.	

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		EP 0315275 A2	10-05-89	LSI Logic		
		WO 93 25968 A1	12-23-93	Furtek		
		EP 0 905 906 A2	03-31-99	Lucent		
		EP 1 235 351 A1	08-28-02	Matsushita Electric		

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				First Named Inventor	Andy H. Gan
				Art Unit	2825
				Examiner Name	Binh C. Tat
Sheet	2	of	7	Attorney Docket Number	X-1068 US

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		SAYFE KIAEI et al., "VLSI DESIGN OF DYNAMICALLY RECONFIGURABLE ARRAY PROCESSOR-DRAP," IEEE, February 1989, pp. 2484-2488, V3.6, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997	
		VASON P. SRINI, "FIELD PROGRAMMABLE GATE ARRAY (FPGA) IMPLEMENTATION OF DIGITAL SYSTEMS: AN ALTERNATIVE TO ASIC," IEEE, May 1991, pp. 309-314, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997	
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		Application / C nt. No.	10/075,178 / 7288
		Filing Date	February 14, 2002
		First Name of Inventor	Andy H. Gan
		Art Unit	2825
		Examiner Name	Binh C. Tat
Sheet 3 of 7	Attorney Docket Number	X-1068 US	

U.S. PATENT DOCUMENTS					
Examiner Initials *	Cite No.¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code (if known)			
		US- 5,473,267	12-05-95	Stansfield	
		US- 5,742,180	04-21-98	DeHon et al.	
		US- 5,737,631	04-07-98	Trimberger	
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		US- 5,933,023	08-03-99	Young	
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		US- 5,874,834	02-23-99	New	
		US- 5,835,405	11-10-98	Isui et al.	

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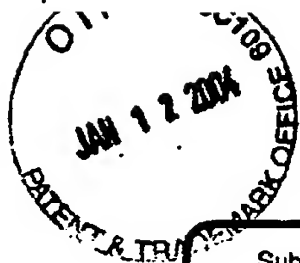
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			Filing Date	February 14, 2002	
			First Named Inventor	Andy H. Gan	
			Art Unit	2825	
			Examiner Name	Binh C. Tat	
Sheet	4	of	7	Attorney Docket Number	X-1068 US

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		XILINX, INC., "THE PROGRAMMABLE LOGIC DATA BOOK," 1994, Revised 1995, Xilinx, Inc., 2100 Logic Drive, San Jose, CA. 95124.	
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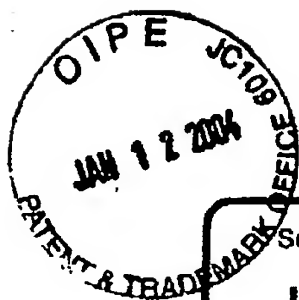
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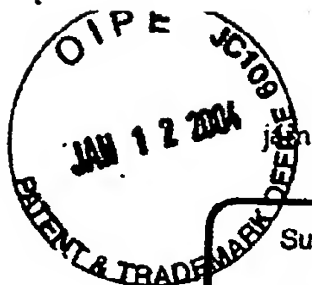
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		US- 6,519,753	02-11-03	Ang	
		US- 6,434,735	08-13-02	Watkins	
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		US- 6,539,508	03-25-03	Patrie et al.	
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